

Attorney Docket No. FUJ 99228 CIP Client Matter. No. 80458.0011

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Katsuyoshi MATSUURA, et al.

Serial No. 09/551,233

Filed: April 17, 2000

Title: SEMICONDUCTOR DEVICE HAVING A

FERROELECTRIC CAPACITOR AND A FABRICATION PROCESS THEREOF

Examiner: H.-M. LEE

Art Unit:

2823

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant hereby submits for filing under 37 CFR 1.97 a disclosure statement. The patents, publications or other information of which Applicant is presently aware are listed in Form PTO/SB/08A submitted herewith. Copies of such patents are enclosed. In submitting these references, no representation is made or implied that the reference is or is not material to the examination of this application.

This Information Disclosure Statement is being filed after the mailing of the first Office Action. Enclosed herewith is a check for \$290.00 which includes the fee for this Information Disclosure Statement. However, any fee deficiency associated with this submittal may be charged to Deposit Account No. 50-1123.

September 3, 2003

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Respectfully submitted,

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PTO/SB/08A (10/01) (Substitute for form 1449A/PTO)

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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FUJ 99228 CIP	

APPLICATION NO 09/551,233

FIRST NAMED INVENTOR: Katsuyoshi MATSUURA

(Use several sheets if necessary) Sheet 1 of 1			FILING DATE: April 17, 2000 ART UNIT: 2823				
			U.S. PATE	NT DOCUMENTS			
Examiner Initials	Cite No.	Document No. No. – Kind Code	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Doc	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
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